EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	635	710/200.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:20
L2	2277	709/225.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:20
L3	672	709/222.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:21
L4	22	(lock$4 and unlock$4) same register$4 same reset$4 same timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:22
L5	111	(lock$4 and unlock$4) same register$4 same reset$4 and timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:32
L6	1	(lock$4 and unlock$4) same register$4 same reset$4 and timer and managed with server$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:23
L7	3	1 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:23
L8	1	2 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:31
L9	0	3 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:23

L10	2	(lock$4 and unlock$4) same register$4 same reset$4 and managed with server$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:34
L11	103	(lock$4 and unlock$4) same register$4 same reset$4 and (((input and output) or I/O) with processor$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:39
L12	13	1 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:34
L13	1	2 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:39
L14	0	3 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:34
L15	39	(lock$4 and unlock$4) same register$4 same reset$4 and (((input and output) or I/O) with processor$4) same operation$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:39
L16	45	(lock$4 and unlock$4) same register$4 same reset$4 and (((input and output) or I/O) with processor$4) same operat$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:40
L17	2	1 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:40
L18	0	2 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:40
L19	0	3 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:40

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L2	2277	709/225.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:20
L3	672	709/222.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:21
L4	22	(lock$4 and unlock$4) same register$4 same reset$4 same timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:22
L5	111	(lock$4 and unlock$4) same register$4 same reset$4 and timer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:32
L6	1	(lock$4 and unlock$4) same register$4 same reset$4 and timer and managed with server$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:53
L7	3	1 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:23
L8	1	2 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:31
L9	0	3 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:23

L10	2	(lock$4 and unlock$4) same register$4 same reset$4 and managed with server$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:54
L11	103	(lock$4 and unlock$4) same register$4 same reset$4 and (((input and output) or I/O) with processor$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:54
L12	13	1 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:34
L13	1	2 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:39
L14	0	3 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:34
L15	39	(lock$4 and unlock$4) same register$4 same reset$4 and (((input and output) or I/O) with processor$4) same operation$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:39
L16	45	(lock$4 and unlock$4) same register$4 same reset$4 and (((input and output) or I/O) with processor$4) same operat$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:40
L17	2	1 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:40
L18	0	2 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:40
L19	0	3 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:40

L20	1	(lock$4 and unlock$4) same (register$4 or memor$4)same reset$4 and timer and managed with server$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:54
L21	2	(lock$4 and unlock$4) same (register$4 or memor$4) same reset$4 and managed with server$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:54
L22	158	(lock$4 and unlock$4) same (register$4 or memor$4) same reset$4 and (((input and output) or I/O) with processor$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:55
L23	23	1 and 22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:55
L24	1	2 and 22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:55
L25	0	3 and 22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/12 09:55

remote server management controller and registers

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...an input/output (I/O) **controller** 18 ... URL corresponding to the **remote server** requesting
authentication is found, the password **management**
www.freepatentsonline.com/6182229.html

3. C H A P T E R 2 - IPMI **Server Management**
It facilitates platform-side **server management and remote server-management** framewoi
providing a ... Baseboard **Management Controller** ... In
docs.sun.com/source/817-5249-15/chapter2.html

4. Directory **Server** Access **Management** Edition Programmer's Guide:...
Directory **Server** Access **Management** Edition Programmer's Guide ... Cross Domain **Controll**
Domain ... **Remote** SSO Sample This sample can
docs.sun.com/source/816-5628-10/sso.html

5. **Server** Security Patch **Management** at Microsoft
...availability, **and** supports centralized **and remote** monitoring **and management**. Operation
When a problem with a fully managed **server** is...

www.microsoft.com/technet/itsolutions/MSIT/Security/SMS03SPM.mspx

6. Microsoft TechNet: Content **Management Server** - Ford Motor Company...

Content **Management Server** - Ford Motor Company Technical Deployment Guide ... Ford.com used a **remote** Ultraseek search **server**, but...
www.microsoft.com/technet/prodtechnol/cms/case/ford.mspx

7. Systems **Management Server** 2003 Operations Release Notes

Systems **Management Server** 2003 Operations Release Notes ... information from the domair ... Using Status Message Viewer on a **Remote**...
xfiles.erin.utoronto.ca/pub/windows/SMS/SMS2003EVAL_ENU/SMS2003EVAL_ENU/rea

8. Microsoft Systems **Management Server** 2003 - Operations Guide

A P P E N D I X B Microsoft® Systems **Management Server** (SMS) 2003 is a ... which uses th< procedure ... type is to replace the **controller**...
spa.its.uiowa.edu/ecm/imaging/SMS/Pages%20from%20Windows%20Management%20Ins

9. White Paper NEC Express5800 **Server**?s Integrated **Remote**

RomPilot BIOS extensions **and** the systems Baseboard **Management Controller** (BMC) provid emergency access **and** control of **server** resources.
www.necsam.com/servers/whitepapers/MWAwhitepaper.pdf

10. Compaq AlphaServer DS20E Systems

? ISA interrupt **controller** ? Speaker ... The system features module provides for the following: **server management** ? Environmental
h18000.www1.hp.com/alphaserver/download/ds20e_tech_summ_0701.pdf

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1 **Optical disk recording and reproducing apparatus, remote controller, and content management method**
Inventor: MIYAKE KUNIHIKO (JP); IIMURA SHINICHIRO Applicant: SONY CORP (JP)
(JP); (+2)
EC: IPC: *G11B7/00*; G11B7/00

Publication info: **US2006013094** - 2006-01-19

2 **PRINT CONTROLLER, PRINT CONTROL METHOD, PROGRAM AND RECORDING MEDIUM REMOTE MANAGEMENT SYSTEM**
Inventor: KUMASHIO HIROYA   Applicant: RICOH KK

EC: IPC: *B41J5/30; B41J29/38; G06F3/12* (+6)

Publication info: **JP2005222267** - 2005-08-18

3. **INFORMATION COMMUNICATION UNIT FOR PROGRAMMABLE CONTROLLER, AND REMOTE MANAGEMENT SYSTEM AND METHOD FOR FACTORY AUTOMATION APPARATUS**
Inventor: AMANO KOJI; INOUE NAOTAKE   Applicant: MITSUBISHI ELECTRIC CORP

EC: IPC: *G05B19/05; G06F13/00*; G05B19/05 (+3)

Publication info: **JP2004213406** - 2004-07-29

4 **Remote-management controller for automobile**
Inventor: GUO JINFENG (CN); ZHANG ZUNZHENG (CN) Applicant: GUO JINFENG (CN)
EC: IPC: *B60R25/00; B60R25/10*; B60R25/00 (+3)

Publication info: **CN2564422Y** - 2003-08-06

5 **Method and apparatus for creating a secure embedded I/O processor for a remote server management controller**
Inventor: EMERSON THEODORE F (US); NOONAN   Applicant:
ROBERT L (US); (+1)
EC: IPC: *G06F9/00; G06F9/24; G06F15/16* (+10)

Publication info: **US2003149796** - 2003-08-07

6 **Method and apparatus for providing JTAG functionality in a remote server management controller**
Inventor: BROWN ANDREW (US); EMERSON THEODORE Applicant:
F (US); (+2)
EC: IPC: *G01R31/28*; G01R31/28; (IPC1-7): G01R31/28

Publication info: **US2003140291** - 2003-07-24

7 **Method and apparatus for passive PCI throttling in a remote server management controller**
Inventor: NOONAN ROBERT L (US); EMERSON   Applicant:
THEODORE F (US)
EC: IPC: *G06F15/16*; G06F15/16; (IPC1-7): G06F15/16

Publication info: **US2003131119** - 2003-07-10

8 **System and method to implement a cost-effective remote system management mechanism using a serial communication controller and interrupts**
Inventor: WANG JENNIFER C (US); JOSHI ANIRUDDHA Applicant:
P (US)
EC: D03D41/00   IPC: *D03D41/00*; D03D41/00; (IPC1-7): G06F13/24

Publication info: **US2003065852** - 2003-04-03

9 **REMOTE MAINTENANCE MANAGEMENT AND TECHNICAL SUPPORT SYSTEM FOR CONTROLLER**

Inventor: SATO TETSUYA; HASEGAWA YUKIO; (+3)

Applicant: HITACHI SERVICE & ENGINEERING

EC:

IPC: *G05B23/02; G06F11/30; G06F13/00* (+12)

Publication info: **JP2002032120** - 2002-01-31

10 METHOD FOR RETRIEVING PERSONAL MANAGEMENT INFORMATION OF TERMINAL USING REMOTE CONTROLLER

Inventor: SONG YEONG A (KR)

Applicant: LG INF & COMM LTD (KR)

EC:

IPC: *H04B1/38*; **H04B1/38**; (IPC1-7): H04B1/38

Publication info: **KR2001085177** - 2001-09-07

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1. 14_5
...**and** data TLBs. Peripheral units are ICU for multi-**processor, Timer**, Serial **I/O**, digital phas
shared bus supports **lock/unlock**...
www.linux-m32r.org/cmn/m32r/ISSCC-2003-14_5.pdf

2. Multi-tasking **register** set mapping system which changes a **register**...
Abstract: An **I/O processor** includes an execution unit (EU), a **register** file, an **I/O** bus seque
above, cachable 1011 **Unlock** Request
www.freepatentsonline.com/4853849.html

3. Memory-based interagent communication mechanism - Patent 4829425
...an **I/O** bus sequencer (12) for providing a data path to external **I/O** devices connected to sai
processor by an **I/O** bus; **and** , a **register**...
www.freepatentsonline.com/4829425.html

4. DOS Protected Mode Interface Version 1.0 Chapter 1. Introduction
...run at Ring 3 so that a host operating environment can fully virtualize hardware interrupts, **I,**
processor exceptions through the 80386's...
clio.rice.edu/cwsdpmi/dpmispec1.pdf

5. CM-388 PC/104 CPU Module
...to 64 MB, Realtime Clock, Watch-dog **Timer**, ISA ... for AT-keyboard Internal 5-pin header fc
mouse High Speed Multi **I/O** ? Chipset : ALi
www.dge.com.au/manuals/cm388trm.pdf

6. **I/O**: 300, VID: 90000a46 Blkmem copyright 1998 1999 D. Jeff Dionne
I/O: 300, VID: 90000a46 Blkmem copyright 1998 1999 D. Jeff Dionne Blkmem ... dentry_oper
lock_page 01027dc0 **unlock**_page 0102f9b4...
www.jacek.it.pl/kiss/kiss-session.txt

7. Microsoft PowerPoint - sysarch17-ioman.ppt
Caching ! Clocks **and Timer** ! Serial **I/O** ... or ! **lock and unlock** ? © 2003 Universität Karlsru
Architecture Group 36 Layers of **I/O**
i30www.ira.uka.de/teaching/coursedocuments/91/sysarch17-ioman.6per1.pdf

8. Linux Kernel: [PATCHSET 3/17] support PC-9800 against 2.5.45-ac1...
I/O space for devices used on all **i**[345]86 PCs ... spin_**unlock**(&rtc_**lock**); return retval
locally disabled */ spin_**lock**(&i8253_**lock**); /* **timer**
seclists.org/linux-kernel/2002/Nov/1969.html

9. AN 298: Reconfiguring Excalibur Devices Under **Processor** Control
Controller **Timer** JTAG 128 Kbytes SRAM 64 Kbytes DPRAM 32 Kbytes SRAM 16 Kbytes DPRAM
SRAM 128 Kbytes DPRAM Embedded **Processor**...
www.altera.com/literature/an/an298.pdf

10. Linux-kernel mailing list archive 2002-14 : Re: sard/iostat disk **I/O**

Re: sard/iostat disk **I/O** statistics/accounting for 2.5.8 ... be on that > second **processor**, obvi 6 +168 10 @@ >

www.cs.helsinki.fi/linux/linux-kernel/2002-14/0851.html

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